

What is claimed is:

1. A testing device for testing the peripheral devices with USB interface, comprising:

5 a CPU;

a memory with a predetermined firmware program, said memory connected with said CPU; and

a control chip with USB interface control, connected with said CPU, having a plurality of connecting ports for connecting with said peripheral devices,

10 wherein said firmware program of said memory is used for controlling said CPU to test said peripheral devices through said control chip.

2. The device of claim 1, wherein said memory is provided with a test packet.

3. The device of claim 2, wherein said CPU is able to send said test packet to said peripheral devices through said control chip.

15 4. The device of claim 3, wherein after receiving said test packet, said peripheral device sends back said test packet to said CPU for determining whether said test packet is the same as predetermined said test packet provided in the memory.

20 5. A testing device, implemented in at least one peripheral device, wherein said peripheral device is provided with a USB interface, said device comprising:

a CPU having a memory with a predetermined firmware program inside; and

a control chip with USB interface, connected to said CPU,

wherein said control chip is provided with a plurality of connecting ports for connecting with said peripheral devices.

5     6. The device of claim 5, wherein said memory is further provided with a test packet inside.

7. The device of claim 6, wherein said CPU is able to send said test packet to said peripheral device through said control chip.

10     8. The device of claim 7, wherein after receiving said test packet, said peripheral device sends said test packet back to said CPU for determining whether said test packet is the same as predetermined said test packet provided in the memory.

9. A testing device for testing at least one peripheral device, wherein said peripheral device is provided with USB interface, characterized by:

15     a CPU, connected with at least one memory, said memory comprising a firmware program for indicating said CPU; and

a control chip with USB interface control, connected to said CPU,

wherein said CPU is able to send a test packet stored in the memory to said peripheral device through said control chip;

20     wherein after receiving said predetermined test packet, said peripheral device sends said test packet back to said CPU for determining whether said test packet is the same as predetermined said test packet stored in the memory.